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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/664,969	09/22/2003	Katsumi Abe	Q75817	4962

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EXAMINER

PHAM, TAMMY T

ART UNIT PAPER NUMBER

2629

DATE MAILED: 06/15/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

10/664,969

Applicant(s)

ABE, KATSUMI

Examiner

Tammy Pham

Art Unit

2629

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 03 March 2006.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-7 and 16-21 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-7, 16-21 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 22 September 2003 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some * c) ☐ None of:
1. ☒ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☒ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date _____.
- 4) ☐ Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____.
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: _____.

DETAILED ACTION

Based upon a restriction requirement that the examiner sent out on 2/8/2006; the applicant has elected Group 1, which consists of claims 1-7 and 16-21 without traverse.

Claim Objections

Claims 4, 19 would be allowable if rewritten to overcome the rejection(s) under 35 U.S.C. 112, 2nd paragraph, set forth in this Office action and to include all of the limitations of the base claim and any intervening claims.

Claim Rejections - 35 USC § 112

The following is a quotation of the second paragraph of 35 U.S.C. 112:

The specification shall conclude with one or more claims particularly pointing out and distinctly claiming the subject matter which the applicant regards as his invention.

Claims 4, 19 are rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention. In particular, the applicant does not point out in the diagram the particularities of claims 4, 19. Appropriate correction is advised.

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for

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patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

Claims 1-3, 5-7, 16-18 and 20-21 are rejected under 35 U.S.C. 102(e) as being anticipated by Ishiyama (US Patent Application No: 2002/0180720 A1).

As for claims 1, 17, Ishiyama teaches of a drive circuit (Fig. 2) comprising:

a first voltage supply (OP1);

a second voltage supply (OP2) for providing a voltage that is lower than a voltage of the first voltage supply (OP1);

at least one first transistor (TG1) including either a drain or a source terminal connected to the first voltage supply (OP1);

at least one second transistor (TG2) including either a drain or source terminal connected to the second voltage supply (OP2);

at least one signal line (SEL1,2) connected to each gate terminal of the first and second transistor (TG2); and

at least one capacitance load (s) connected to respective terminals of the first and the second transistor (TG2)s not connected to the first and second voltage supplies,

wherein the signal line (SEL1,2) conveys signals having a high level that is substantially the same or higher than the voltage of the first voltage supply (OP1) and having a low level that is substantially the same or lower than the voltage of the second voltage supply (OP2) in Fig. 18 and in section [0237].

As for claim 2, Ishiyama teaches that at least the drive circuit (Fig. 2), a display portion (12) and a gate driver circuit for controlling switching of pixels of each line in the display portion (12) are mounted on a substrate, and

wherein the driving circuit is disposed on a position opposite to the gate driver circuit and the display portion (12) therebetween in Fig. 1.

As for claims 3, 18, Ishiyama teaches that at least one first transistor (TG1) comprises P-type transistor and the at least one second transistor (TG2)s comprises N-type transistor, and

wherein the gate terminals of the first and second transistor (TG2)s are connected to common signal line (SEL1,2)s in Fig. 18.

As for claims 5, 20, Ishiyama teaches that a high-level voltage of each signal of the signal line (SEL1,2) and the inversion signal line (SEL1,2) is a high-level line voltage of the gate driver and

wherein a low-level voltage of each signal of the signal line (SEL1,2) and the inversion signal line (SEL1,2) is a low-level line voltage of the gate driver in sections [0251-2].

As for claims 6, 21, Ishiyama teaches that the first and second transistor (TG2)s are comprised of thin-film transistors in section [0230].

As for claim 7, Ishiyama teaches that the display portion (12) comprises a liquid crystal display in Fig. 1.


As for claim 16, Ishiyama teaches that a display comprising:
a substrate;
a display portion (12) integrated on the substrate;
a gate driver circuit for controlling switching of pixels of each line in a display portion (12);
a drive circuit (Fig. 2) for the display portion (12) for simultaneously driving capacitance load (s)s in the display portion (12),
wherein the drive circuit (Fig. 2) is disposed on a position opposite to the gate driver circuit and the display portion (12) therebetween in Fig. 1.

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Tammy Pham whose telephone number is (571) 272-7773. The examiner can normally be reached on 8:00-5:30 (Mon-Fri).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Sumati Lefkowitz can be reached on (571) 272-3638. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.


Tammy Pham
May 30, 2006


KENT CHANG
PRIMARY EXAMINER